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Fundamentals of Microelectronics

John Wiley & Sons **Fundamentals of Microelectronics, 2nd Edition** is designed to build a strong foundation in both design and analysis of electronic circuits this text offers conceptual understanding and mastery of the material by using modern examples to motivate and prepare readers for advanced courses and their careers. The book's unique problem-solving framework enables readers to deconstruct complex problems into components that they are familiar with which builds the confidence and intuitive skills needed for success.

Design of Analog CMOS Integrated Circuits

McGraw-Hill Companies **This textbook deals with the analysis and design of analog CMOS integrated circuits, emphasizing recent technological developments and design paradigms that students and practicing engineers need to master to succeed in today's industry. Based on the author's teaching and research experience in the past ten years, the text follows three general principles: (1) Motivate the reader by describing the significance and application of each idea with real-world problems; (2) Force the reader to look at concepts from an intuitive point of view, preparing him/her for more complex problems; (3) Complement the intuition by rigorous analysis, confirming the results obtained by the intuitive, yet rough approach.**

Microelectronics

Wiley **By helping students develop an intuitive understanding of the subject, Microelectronics teaches them to think like engineers. The second edition of Razavi's Microelectronics retains its hallmark emphasis on analysis by inspection and building students' design intuition, and it incorporates a host of new pedagogical features that make it easier to teach and learn from, including: application sidebars, self-check problems with answers, simulation problems with SPICE and MULTISIM, and an expanded problem set that is organized by degree of difficulty and more clearly associated with specific chapter sections.**

Design of CMOS Phase-Locked Loops

From Circuit Level to Architecture Level

Cambridge University Press **This modern, pedagogic textbook from leading author Behzad Razavi provides a comprehensive and rigorous introduction to CMOS PLL design, featuring intuitive presentation of theoretical concepts, extensive circuit simulations, over 200 worked examples, and 250 end-of-chapter problems. The perfect text for senior undergraduate and graduate students.**

RF Microelectronics

Prentice Hall **The Acclaimed RF Microelectronics Best-Seller, Expanded and Updated for the Newest Architectures, Circuits, and Devices** Wireless communication has become almost as ubiquitous as electricity, but RF design continues to challenge engineers and researchers. In the 15 years since the first edition of this classic text, the demand for higher performance has led to an explosive growth of RF design techniques. In *RF Microelectronics, Second Edition*, Behzad Razavi systematically teaches the fundamentals as well as the state-of-the-art developments in the analysis and design of RF circuits and transceivers. Razavi has written the second edition to reflect today's RF microelectronics, covering key topics in far greater detail. At nearly three times the length of the first edition, the second edition is an indispensable tome for both students and practicing engineers. With his lucid prose, Razavi now offers a stronger tutorial focus along with hundreds of examples and problems. Teaches design as well as analysis with the aid of step-by-step design procedures and a chapter dedicated to the design of a dual-band WiFi transceiver. Describes new design paradigms and analysis techniques for circuits such as low-noise amplifiers, mixers, oscillators, and frequency dividers. This edition's extensive coverage includes brand new chapters on mixers, passive devices, integer-N synthesizers, and fractional-N synthesizers. Razavi's teachings culminate in a new chapter that begins with WiFi's radio specifications and, step by step, designs the transceiver at the transistor level. Coverage includes Core RF principles, including noise and nonlinearity, with ties to analog design, microwave theory, and communication systems. An intuitive treatment of modulation theory and wireless standards from the standpoint of the RF IC designer. Transceiver architectures such as heterodyne, sliding-IF, directconversion, image-reject, and low-IF topologies. Low-noise amplifiers, including cascode common-gate and common-source topologies, noise-cancelling schemes, and reactance-cancelling configurations. Passive and active mixers, including their gain and noise analysis and new mixer topologies. Voltage-controlled oscillators, phase noise mechanisms, and various VCO topologies dealing with noise-power-tuning trade-offs. All-new coverage of passive devices, such as integrated inductors, MOS varactors, and transformers. A chapter on the analysis and design of phase-locked loops with emphasis on low phase noise and low spur levels. Two chapters on integer-N and fractional-N synthesizers, including the design of frequency dividers. Power amplifier principles and circuit topologies along with transmitter architectures, such as polar modulation and outphasing.

CMOS (CMOS—CMOS))

CMOS, CMOS, MOS.

Health Economics

Bloomsbury Publishing **Comprehensive in coverage this textbook, written by academics from leading institutions, discusses current developments and debates in modern health economics from an international perspective. Economic models are presented in detail, complemented by real-life explanations and analysis, and discussions of the influence of such theories on policymaking. Offering sound pedagogy and economic rigor, Health Economics focuses on building intuition alongside appropriate mathematical formality, translating technical language into accessible economic narrative. Rather than shying away from intellectual building blocks, students are introduced to technical and theoretical foundations and encouraged to apply these to inform empirical studies and wider policymaking. Health Economics provides: - A broad scope, featuring comparative health policy and empirical examples from around the world to help students relate the principles of health economics to everyday life - Coverage of topical issues such as the obesity epidemic, economic epidemiology, socioeconomic health disparities, and behavioural economics - A rich learning resource, complete with hundreds of exercises to help solidify and extend understanding. This book is designed for advanced undergraduate courses in health economics and policy but may also interest postgraduate students in economics, medicine and health policy.**

Radio Frequency Integrated Circuits and Systems

Cambridge University Press **Equips students with essential industry-relevant knowledge through in-depth explanations, practical applications, examples, and exercises.**

Applied Electromagnetics

Early Transmission Lines Approach

John Wiley & Sons **STUDENT COMPANION SITE** Every new copy of Stuart Wentworth's *Applied Electromagnetics* comes with a registration code which allows access to the Student's Book Companion Site. On the BCS the student will find: * Detailed Solutions to Odd-Numbered Problems in the text * Detailed Solutions to all Drill Problems from the text * MATLAB code for all the MATLAB examples in the text * Additional MATLAB demonstrations with code. This includes a Transmission Lines simulator created by the author. * Weblinks to a vast array of resources for the engineering student. Go to www.wiley.com/college/wentworth to link to *Applied Electromagnetics* and the Student Companion Site. **ABOUT THE PHOTO** Passive RFID systems, consisting of readers and tags, are expected to replace bar codes as the primary means of identification, inventory and billing of everyday items. The tags typically consist of an RFID chip placed on a flexible film containing a planar antenna. The antenna captures radiation from the reader's signal to power the tag electronics, which then responds to the reader's query. The PENI Tag (Product Emitting Numbering Identification Tag) shown, developed by the University of Pittsburgh in a team led by Professor Marlin H. Mickle, integrates the antenna with the rest of the tag electronics. RFID systems involve many electromagnetics concepts, including antennas, radiation,

transmission lines, and microwave circuit components. (Photo courtesy of Marlin H. Mickle.)

CMOS Digital Integrated Circuits

Analysis and Design

The fourth edition of *CMOS Digital Integrated Circuits: Analysis and Design* continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability.

Introduction to Probability Models

Elsevier Ross classic bestseller has been used extensively by professionals and as the primary text for a first undergraduate course in applied probability. With the addition of several new sections relating to actuaries, this text is highly recommended by the Society of Actuaries.

Digital Design: Principles And Practices, 4/E

Pearson Education India

Design of Analog CMOS Integrated Circuits

CMOS analog circuit design

Circuits

NTS Press

Modeling of Carbon Nanotubes, Graphene and their Composites

Springer Science & Business Media A large part of the research currently being conducted in the fields of materials science and engineering mechanics is devoted to carbon nanotubes and their applications. In this process, modeling is a very attractive investigation tool due to the difficulties in manufacturing and testing of nanomaterials. Continuum modeling offers significant advantages over atomistic modeling. Furthermore, the lack of accuracy in continuum methods can be overtaken by incorporating input data either from experiments or atomistic methods. This book reviews the recent progress in continuum modeling of carbon nanotubes and their composites. The advantages and disadvantages of continuum methods over atomistic methods are comprehensively discussed. Numerical models, mainly based on the finite element method, as well as analytical models are presented in a comparative way starting from the simulation of isolated pristine and defected nanotubes and proceeding to nanotube-based composites. The ability of continuum methods to bridge different scales is emphasized. Recommendations for future research are given by focusing on what still continuum methods have to learn from the nano-scale. The scope of the book is to provide current knowledge aiming to support researchers entering the scientific area of carbon nanotubes to choose the appropriate modeling tool for accomplishing their study and place their efforts to further improve continuum methods.

Fundamentals of Modern VLSI Devices

Cambridge University Press Learn the basic properties and designs of modern VLSI devices, as well as the factors affecting performance, with this thoroughly updated second edition. The first edition has been widely adopted as a standard textbook in microelectronics in many major US universities and worldwide. The internationally renowned authors highlight the intricate interdependencies and subtle trade-offs between various practically important device parameters, and provide an in-depth discussion of device scaling and scaling limits of CMOS and bipolar devices. Equations and parameters provided are checked continuously against the reality of silicon data, making the book equally useful in practical transistor design and in the classroom. Every chapter has been updated to include the latest developments, such as MOSFET scale length theory, high-field transport model and SiGe-base bipolar devices.

Monolithic Phase-Locked Loops and Clock Recovery Circuits

Theory and Design

John Wiley & Sons Featuring an extensive 40 page tutorial introduction, this carefully compiled anthology of 65 of the most important papers on phase-locked loops and clock recovery circuits brings you comprehensive coverage of the field—all in one self-contained volume. You'll gain an understanding of the analysis, design, simulation, and implementation of phase-locked loops and clock recovery circuits in CMOS and bipolar technologies along with valuable insights into the issues and trade-offs associated with phase locked systems for high speed, low power, and low noise.

Solid State Radio Engineering

John Wiley & Sons Incorporated A comprehensive text that covers both receiver and transmitter circuits, reflecting the past decade's developments in solid-state technology. Emphasizes design using practical circuit elements, with basic ideas of electrical noise, resonant impedance-matching circuits, and modulation theory thoroughly explained. Contains the latest techniques in radio frequency power amplifier design, accepted state-of-the-art technology based on bipolar junction transistors, VMOS RF power FETs, high-efficiency techniques, envelope elimination and restoration, envelope feedback, and other newly emerging technologies. Requires a knowledge of complex algebra, Fourier series, and Fourier transforms. Also includes numerous worked-out examples that relate the theory to practical circuit applications, and homework problems keyed to corresponding sections of the text.

KC's Problems and Solutions for Microelectronic Circuits, Fourth Edition

New York : Oxford University Press This manual includes hundreds of problem and solutions of varying degrees of difficulty for student review. The solutions are completely worked out to facilitate self-study.

CMOS

Circuit Design, Layout, and Simulation

John Wiley & Sons Praise for *CMOS: Circuit Design, Layout, and Simulation* Revised Second Edition from the Technical Reviewers "A refreshing industrial flavor. Design concepts are presented as they are needed for 'just-in-time' learning. Simulating and designing circuits using SPICE is emphasized with literally hundreds of examples. Very few textbooks contain as much detail as this one. Highly recommended!" --Paul M. Furth, New Mexico State University "This book builds a solid knowledge of CMOS circuit design from the ground up. With coverage of process integration, layout, analog and digital models, noise mechanisms, memory circuits, references, amplifiers, PLLs/DLLs, dynamic circuits, and data converters, the text is an excellent reference for both experienced and novice designers alike." --Tyler J. Gomm, Design Engineer, Micron Technology, Inc. "The Second Edition builds upon the success of the first with new chapters that cover additional material such as oversampled converters and non-volatile memories. This is becoming the de facto standard textbook to have on every analog and mixed-signal designer's bookshelf." --Joe Walsh, Design Engineer, AMI Semiconductor CMOS circuits from design to implementation *CMOS: Circuit Design, Layout, and Simulation, Revised Second Edition* covers the practical design of both analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and much more. This edition takes a two-path approach to the topics: design techniques are developed for both long- and short-channel CMOS technologies and then compared. The results are multidimensional explanations that allow readers to gain deep insight into the design process. Features include: Updated materials to reflect CMOS technology's movement into nanometer sizes Discussions on phase- and delay-locked loops, mixed-signal circuits, data converters, and circuit noise More than 1,000 figures, 200 examples, and over 500 end-of-chapter problems In-depth coverage of both analog and digital circuit-level design

techniques Real-world process parameters and design rules The book's Web site, CMOSedu.com, provides: solutions to the book's problems; additional homework problems without solutions; SPICE simulation examples using HSPICE, LTspice, and WinSpice; layout tools and examples for actually fabricating a chip; and videos to aid learning

Vibration of Mechanical Systems

Cambridge University Press This is a textbook for a first course in mechanical vibrations. There are many books in this area that try to include everything, thus they have become exhaustive compendiums, overwhelming for the undergraduate. In this book, all the basic concepts in mechanical vibrations are clearly identified and presented in a concise and simple manner with illustrative and practical examples. Vibration concepts include a review of selected topics in mechanics; a description of single-degree-of-freedom (SDOF) systems in terms of equivalent mass, equivalent stiffness, and equivalent damping; a unified treatment of various forced response problems (base excitation and rotating balance); an introduction to systems thinking, highlighting the fact that SDOF analysis is a building block for multi-degree-of-freedom (MDOF) and continuous system analyses via modal analysis; and a simple introduction to finite element analysis to connect continuous system and MDOF analyses. There are more than sixty exercise problems, and a complete solutions manual. The use of MATLAB® software is emphasized.

Analog Integrated Circuit Design

The 2nd Edition of Analog Integrated Circuit Design focuses on more coverage about several types of circuits that have increased in importance in the past decade. Furthermore, the text is enhanced with material on CMOS IC device modeling, updated processing layout and expanded coverage to reflect technical innovations. CMOS devices and circuits have more influence in this edition as well as a reduced amount of text on BiCMOS and bipolar information. New chapters include topics on frequency response of analog ICs and basic theory of feedback amplifiers.

Principles of Digital Communication

A Top-Down Approach

Microwave Engineering

John Wiley & Sons Pozar's new edition of Microwave Engineering includes more material on active circuits, noise, nonlinear effects, and wireless systems. Chapters on noise and nonlinear distortion, and active devices have been added along with the coverage of noise and more material on intermodulation distortion and related nonlinear effects. On active devices, there's more updated material on bipolar junction and field effect transistors. New and updated material on wireless communications systems, including link budget, link margin, digital modulation methods, and bit error rates is also part of the new edition. Other new material includes a section on transients on transmission lines, the theory of power waves, a discussion of higher order modes and frequency effects for microstrip line, and a discussion of how to determine unloaded.

Digital Integrated Circuit Design

From VLSI Architectures to CMOS Fabrication

Cambridge University Press Top-down approach to practical, tool-independent, digital circuit design, reflecting how circuits are designed.

Op Amps for Everyone

Design Reference

Newnes The operational amplifier ("op amp") is the most versatile and widely used type of analog IC, used in audio and voltage amplifiers, signal conditioners, signal converters, oscillators, and analog computing systems. Almost every electronic device uses at least one op amp. This book is Texas Instruments' complete professional-level tutorial and reference to operational amplifier theory and applications. Among the topics covered are basic op amp physics (including reviews of current and voltage division, Thevenin's theorem, and transistor models), idealized op amp operation and configuration, feedback theory and methods, single and dual supply operation, understanding op amp parameters, minimizing noise in op amp circuits, and practical applications such as instrumentation amplifiers, signal conditioning, oscillators, active filters, load and level conversions, and analog computing. There is also extensive coverage of circuit construction techniques, including circuit board design, grounding, input and output isolation, using decoupling capacitors, and frequency characteristics of passive components. The material in this book is applicable to all op amp ICs from all manufacturers, not just TI. Unlike textbook treatments of op amp theory that tend to focus on idealized op amp models and configuration, this title uses idealized models only when necessary to explain op amp theory. The bulk of this book is on real-world op amps and their applications; considerations such as thermal effects, circuit noise, circuit buffering, selection of appropriate op amps for a given application, and unexpected effects in passive components are all discussed in detail. *Published in conjunction with Texas Instruments *A single volume, professional-level guide to op amp theory and applications *Covers circuit board layout techniques for manufacturing op amp circuits.

Engineering Circuit Analysis

RF Circuit Design

Theory and Applications

For upper-level Electrical Engineering introductory courses in RF Circuit Design and analog integrated circuits. This practical and comprehensive book introduces RF circuit design fundamentals with an emphasis on design methodologies. * Provides MATLAB routines to carry out simple transmission line computations and allow the graphical display of the resulting impedance behaviors as part of the Smith Chart. * Allows students to implement these software tools on their own PC. All m-files will be included on a bound in CD-ROM. * Presents RF Amplifier Designs, including small and large signal designs, narrow versus broad band, low noise, and many others. * Provides students with useful broad-based knowledge of common amplifier designs used in the industry. * Discusses Matching Networks, such as T and P matching networks and single and double stub matching. It also includes Discrete and Microstrip Line matching techniques with computer simulations... * Presents Scattering parameters such as realistic listings of S-parameters for transistors and transmission line. * Highlights practical use of S-parameters in circuit design and performance evaluation. resistor, capacitor, and inductor networks. It also includes simulations in MATLAB to provide graphical display of circuit behavior and performance analysis. * Introduces the Smith Chart as a design tool to monitor electric behavior of circuits. * Introduces the generic forms of Oscillators and Mixers, including negative resistance condition, fixed-frequency, and YIG-tuned designs. * Explains the most common oscillator designs used in many RF systems. * Provides an overview of common filter types, including low, high, bandpass, Butterworth, and Chebyshev filters. * Provides design tools to enable students to develop a host of practically realizable filters. * Discusses the high-frequency behavior of common circuit components, including the behavior of resistors, capacitors, and inductors. * Helps students understand the difference of low versus high frequency responses. * Introduces the theory of distributed parameters through a discussion on Transmission Lines. This includes line parameters, sources and load terminations, and voltage and current waves. circuits. * Analyzes active/passive RF circuits through various network description models, especially the two-port network. This discussion also covers impedance, admittance, ABCD, h-parameter networks, and interrelations. * Includes a number of important pedagogical features--Intersperses examples throughout each chapter, and includes self-written MATLAB routines and circuit simulations by a commercial RF software package. * Assists students by clarifying and explaining the theoretical developments.

Fundamentals of Machine Elements

McGraw-Hill Science, Engineering & Mathematics Provides undergraduates and practicing engineers with an understanding of the theory and applications behind the fundamental concepts of machine elements. This text includes examples and homework problems designed to test student understanding and build their skills in analysis and design.

Designing Analog Chips

Virtualbookworm Publishing A comprehensive introduction to CMOS and bipolar analog IC design. The book presumes no prior knowledge of linear design, making it comprehensible to engineers with a non-analog background. The emphasis is on practical design, covering the entire field with hundreds of examples to explain the choices. Concepts are presented following the history of their discovery. Content: 1. Devices Semiconductors, The Bipolar Transistor, The Integrated Circuit, Integrated NPN Transistors, The Case of the Lateral PNP Transistor, CMOS Transistors, The Substrate PNP Transistor, Diodes, Zener Diodes, Resistors, Capacitors, CMOS vs. Bipolar; 2. Simulation, DC Analysis, AC Analysis, Transient Analysis, Variations, Models, Diode Model, Bipolar Transistor Model, Model for the Lateral PNP Transistor, MOS Transistor Models, Resistor Models, Models for Capacitors; 3.

Current Mirrors; 4. Differential Pairs; 5. Current Sources; 6. Time Out: Analog Measures, dB, RMS, Noise, Fourier Analysis, Distortion, Frequency Compensation; 7. Bandgap References; 8. Op Amps; 9. Comparators; 10. Transimpedance Amplifiers; 11. Timers and Oscillators; 12. Phase-Locked Loops; 13. Filters; 14. Power, Linear Regulators, Low Drop-Out Regulators, Switching Regulators, Linear Power Amplifiers, Switching Power Amplifiers; 15. A to D and D to A, The Delta-Sigma Converter; 16. Odds and Ends, Gilbert Cell, Multipliers, Peak Detectors, Rectifiers and Averaging Circuits, Thermometers, Zero-Crossing Detectors; 17. Layout.

Electric Circuits and Networks

Pearson Education India **Electric Circuits and Networks** is designed to serve as a textbook for a two-semester undergraduate course on basic electric circuits and networks. The book builds on the subject from its basic principles. Spread over seventeen chapters, the book can be taught with varying degree of emphasis on its six subsections based on the course requirement. Written in a student-friendly manner, its narrative style places adequate stress on the principles that govern the behaviour of electric circuits and networks.

Electric Power Systems

A First Course

John Wiley & Sons Author Ned Mohan has been a leader in EES education and research for decades. His three-book series on Power Electronics focuses on three essential topics in the power sequence based on applications relevant to this age of sustainable energy such as wind turbines and hybrid electric vehicles. The three topics include power electronics, power systems and electric machines. Key features in the first Edition build on Mohan's successful MNPERE texts; his systems approach which puts dry technical detail in the context of applications; and substantial pedagogical support including PPT's, video clips, animations, clicker questions and a lab manual. It follows a top-down systems-level approach to power electronics to highlight interrelationships between these sub-fields. It's intended to cover fundamental and practical design. This book also follows a building-block approach to power electronics that allows an in-depth discussion of several important topics that are usually left. Topics are carefully sequenced to maintain continuity and interest.

Analysis and Design of Analog Integrated Circuits, 5th Edition

Wiley Global Education This is the only comprehensive book in the market for engineers that covers the design of CMOS and bipolar analog integrated circuits. The fifth edition retains its completeness and updates the coverage of bipolar and CMOS circuits. A thorough analysis of a new low-voltage bipolar operational amplifier has been added to Chapters 6, 7, 9, and 11. Chapter 12 has been updated to include a fully differential folded cascode operational amplifier example. With its streamlined and up-to-date coverage, more engineers will turn to this resource to explore key concepts in the field.

Fundamentals of Photonics

Wiley-Interscience In recent years, photonics has found increasing applications in such areas as communications, signal processing, computing, sensing, display, printing, and energy transport. Now, **Fundamentals of Photonics** is the first self-contained introductory-level textbook to offer a thorough survey of this rapidly expanding area of engineering and applied physics. Featuring a logical blend of theory and applications, coverage includes detailed accounts of the primary theories of light, including ray optics, wave optics, electromagnetic optics, and photon optics, as well as the interaction of light with matter, and the theory of semiconductor materials and their optical properties. Presented at increasing levels of complexity, these sections serve as building blocks for the treatment of more advanced topics, such as Fourier optics and holography, guidedwave and fiber optics, photon sources and detectors, electro-optic and acousto-optic devices, nonlinear optical devices, fiber-optic communications, and photonic switching and computing. Included are such vital topics as: Generation of coherent light by lasers, and incoherent light by luminescence sources such as light-emitting diodes Transmission of light through optical components (lenses, apertures, and imaging systems), waveguides, and fibers Modulation, switching, and scanning of light through the use of electrically, acoustically, and optically controlled devices Amplification and frequency conversion of light by the use of wave interactions in nonlinear materials Detection of light by means of semiconductor photodetectors Each chapter contains summaries, highlighted equations, problem sets and exercises, and selected reading lists. Examples of real systems are included to emphasize the concepts governing applications of current interest, and appendices summarize the properties of one- and two-dimensional Fourier transforms, linear-systems theory, and modes of linear systems. An Instructor's Manual presenting detailed solutions to all the problems in the book is available from the Wiley editorial department.

Design of Integrated Circuits for Optical Communications

John Wiley & Sons The only book on integrated circuits for optical communications that fully covers High-Speed IOs, PLLs, CDRs, and transceiver design including optical communication The increasing demand for high-speed transport of data has revitalized optical communications, leading to extensive work on high-speed device and circuit design. With the proliferation of the Internet and the rise in the speed of microprocessors and memories, the transport of data continues to be the bottleneck, motivating work on faster communication channels. Design of Integrated Circuits for Optical Communications, Second Edition deals with the design of high-speed integrated circuits for optical communication transceivers. Building upon a detailed understanding of optical devices, the book describes the analysis and design of critical building blocks, such as transimpedance and limiting amplifiers, laser drivers, phase-locked loops, oscillators, clock and data recovery circuits, and multiplexers. The Second Edition of this bestselling textbook has been fully updated with: A tutorial treatment of broadband circuits for both students and engineers New and unique information dealing with clock and data recovery circuits and multiplexers A chapter dedicated to burst-mode optical communications A detailed study of new circuit developments for optical transceivers An examination of recent implementations in CMOS technology This text is ideal for senior graduate students and engineers involved in high-speed circuit design for optical communications, as well as the more general field of wireline communications.

Physical Design Essentials

An ASIC Design Implementation Perspective

Springer Science & Business Media Arranged in a format that follows the industry-common ASIC physical design flow, **Physical Design Essentials** begins with general concepts of an ASIC library, then examines floorplanning, placement, routing, verification, and finally, testing. Among the topics covered are Basic standard cell design, transistor-sizing, and layout styles; Linear, non-linear, and polynomial characterization; Physical design constraints and floorplanning styles; Algorithms used for placement; Clock Tree Synthesis; Parasitic extraction; Electronic Testing, and many more.

Digital Fundamentals

Pearson Education India

Applications and Design with Analog Integrated Circuits

A guide to the use of analog integrated circuits. Coverage is provided of computer analysis and problem-solving using MICROCAP and PSpice, switched capacitor active filters, operational amplifier characteristics and nonlinear circuits.

Digital Integrated Circuits

John Wiley & Sons Contains the most extensive coverage of digital integrated circuits available in a single source. Provides complete qualitative descriptions of circuit operation followed by in-depth analytical analyses and spice simulations. The circuit families described in detail are transistor-transistor logic (TTL, STTL, and ASTTL), emitter-coupled logic (ECL), NMOS logic, CMOS logic, dynamic CMOS, BiCMOS structures and various GASFET technologies. In addition to detailed presentation of the basic inverter circuits for each digital logic family, complete details of other logic circuits for these families are presented.