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STUDY GUIDE TO BASIC VLSI DESIGN

BASIC VLSI DESIGN

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ESSENTIALS OF VLSI CIRCUITS AND SYSTEMS

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FUNDAMENTALS OF DIGITAL LOGIC DESIGN, WITH VLSI APPLICATIONS

Hardware -- Logic Design.

VLSI DESIGN

I. K. International Pvt Ltd Aimed primarily for undergraduate students pursuing courses in VLSI design, the book emphasizes the physical understanding of underlying principles of the subject. It not only focuses on circuit design process obeying VLSI rules but also on technological aspects of Fabrication. VHDL modeling is discussed as the design engineer is expected to have good knowledge of it. Various Modeling issues of VLSI devices are focused which includes necessary device physics to the required level. With such an in-depth coverage and practical approach practising engineers can also use this as ready reference.

DIGITAL VLSI DESIGN

PHI Learning Pvt. Ltd.

MULTIPROCESSOR COMPUTER ARCHITECTURES

North Holland For over forty years, most of the world has designed its computers after a single fashion. This style, which has come to be known as the von Neumann model after one of its originators, is showing increasing signs of being inadequately powerful for future needs. Efforts to discover alternative models, for so long a trickle, are at least becoming a flood. This book reports on the current status of a number of these efforts. This book is addressed to two main audiences. First, it is aimed at the reader who is new to the world of non-von Neumann computers, either in the area of design or that of application. By taking the form of a catalogue of projects bound in a single volume, it serves as a particularly convenient introductory text. Thus, it can be used to obtain a feel for the problems which designers are presently confronting and the solutions which they are developing. Indeed, the newcomer to the field, with the benefit of a fresh and unbiased viewpoint, may be in the best position to read through these collected reports and to identify some common point which has hitherto gone unnoticed or unexploited. Second, the book is aimed at the reader who is experienced in one particular area or discipline of non-von Neumann computer design. It serves as an introduction to hardware design for the computer scientist, and as an introduction to parallel programming philosophy for the electronics engineer.

ANALOG AND MIXED MODE VLSI DESIGN

PHI Learning Pvt. Ltd.

DIGITAL INTEGRATED CIRCUIT DESIGN

FROM VLSI ARCHITECTURES TO CMOS FABRICATION

Cambridge University Press **Top-down approach to practical, tool-independent, digital circuit design, reflecting how circuits are designed.**

DIGITAL VLSI SYSTEMS DESIGN

A DESIGN MANUAL FOR IMPLEMENTATION OF PROJECTS ON FPGAS AND ASICS USING VERILOG

Springer Science & Business Media **This book provides step-by-step guidance on how to design VLSI systems using Verilog. It shows the way to design systems that are device, vendor and technology independent. Coverage presents new material and theory as well as synthesis of recent work with complete Project Designs using industry standard CAD tools and FPGA boards. The reader is taken step by step through different designs, from implementing a single digital gate to a massive design consuming well over 100,000 gates. All the design codes developed in this book are Register Transfer Level (RTL) compliant and can be readily used or amended to suit new projects.**

VLSI DESIGN

The second edition of VLSI Design is a comprehensive textbook designed for undergraduate students of electrical, electronics, and electronics and communication engineering. It provides a thorough understanding of the fundamental concepts and design of VLSI systems.

MODERN VLSI DESIGN

IP-BASED DESIGN

Prentice-Hall PTR **The award-winning VLSI design guide is now fully updated to reflect the latest advances in chip design**

STATIC TIMING ANALYSIS FOR NANOMETER DESIGNS

A PRACTICAL APPROACH

Springer Science & Business Media **Timing, timing, timing! That is the main concern of a digital designer charged with designing a semiconductor chip. What is it, how is it described, and how does one verify it? The design team of a large digital design may spend months architecting and iterating the design to achieve the required timing target. Besides functional verification, the timing closure is the major milestone which dictates when a chip can be released to the semiconductor foundry for fabrication. This book addresses the timing verification using static timing analysis for nanometer designs. The book has originated from many years of our working in the area of timing verification for complex nanometer designs. We have come across many design engineers trying to learn the background and various aspects of static timing analysis. Unfortunately, there is no book currently available that can be used by a working engineer to get acquainted with the details of static timing analysis. The chip designers lack a central reference for information on timing, that covers the basics to the advanced timing verification procedures and techniques.**

CMOS VLSI DESIGN: A CIRCUITS AND SYSTEMS PERSPECTIVE

Pearson Education India

INTRODUCTION TO VLSI CIRCUITS AND SYSTEMS

John Wiley & Sons Incorporated **CD-ROM contains: AIM SPICE (from AIM Software) -- Micro-Cap 6 (from Spectrum Software) -- Silos III Verilog Simulator (from Simucad) -- Adobe Acrobat Reader 4.0 (from Adobe).**

PRACTICAL LOW POWER DIGITAL VLSI DESIGN

Springer Science & Business Media **Practical Low Power Digital VLSI Design emphasizes the optimization and trade-off techniques that involve power dissipation, in the hope that the readers are better prepared the next time they are presented with a low power design problem. The book highlights the basic principles, methodologies and techniques that are common to most CMOS digital designs. The advantages and disadvantages of a particular low power technique are discussed. Besides the classical area-performance trade-off, the impact to design cycle time, complexity, risk, testability and reusability are discussed. The wide impacts to all aspects of design are what make low power problems challenging and interesting. Heavy emphasis is given to top-down structured design style, with occasional coverage in the semicustom design methodology. The examples and design techniques cited have been known to be applied to production scale designs or laboratory settings. The goal of Practical Low Power Digital VLSI Design is to permit the readers to practice the low power techniques using current generation design style and process technology. Practical Low Power Digital VLSI Design considers a wide range of design abstraction levels spanning circuit, logic, architecture and system. Substantial basic knowledge is provided for qualitative and quantitative analysis at the different design abstraction levels. Low power techniques are presented at the circuit, logic, architecture and system levels. Special techniques that are specific to some key areas of digital chip design are discussed as well as some of the low power techniques that are just appearing on the horizon. Practical Low Power Digital VLSI Design will be of benefit to VLSI design engineers and students who have a fundamental knowledge of CMOS digital design.**

BROADBAND ACCESS NETWORKS

INTRODUCTION STRATEGIES AND TECHNO-ECONOMIC EVALUATION

Springer Science & Business Media **The access network is expected to be one of the major battlegrounds of telecommunications network operators, since upgrades of the existing narrowband access network will be the critical factor in supplying multimedia broadband services in a competitive market. The future broadband access network architecture needs to be flexible enough to efficiently support the provision of a full set of broadband and narrowband services with a wide range of capacity demands. A wide range of broadband access technologies are available. Furthermore, the key issues in the upgrading of the very cost sensitive access network are financial as well as technological, both for incumbent and new entrant operators. Thus, in order to identify minimum-risk introductory strategies the economic viability of access network broadband upgrades needs to be carefully assessed. However, despite the definite need for techno-economic evaluations, very few books have been published in this field. One of the reasons might be that broadband access network upgrading only very recently gained wide recognition as a key challenge for broadband delivery. Secondly, this kind of strategic work and these studies tend to be considered rather sensitive by operators, and thus both results and methodologies are not usually readily available. Thirdly, the work reported in this book in many respects was a major pioneering effort, which quite ambitiously aimed at modelling the whole life-cycle costs and revenue streams of access network upgrades, as opposed to several other efforts, which often are limited to pure investment cost comparisons.**

VLSI DESIGN

BoD – Books on Demand **This book provides some recent advances in design nanometer VLSI chips. The selected topics try to present some open problems and challenges with important topics ranging from design tools, new post-silicon devices, GPU-based parallel**

computing, emerging 3D integration, and antenna design. The book consists of two parts, with chapters such as: VLSI design for multi-sensor smart systems on a chip, Three-dimensional integrated circuits design for thousand-core processors, Parallel symbolic analysis of large analog circuits on GPU platforms, Algorithms for CAD tools VLSI design, A multilevel memetic algorithm for large SAT-encoded problems, etc.

CMOS

CIRCUIT DESIGN, LAYOUT, AND SIMULATION

[John Wiley & Sons](#) **Praise for CMOS: Circuit Design, Layout, and Simulation** Revised Second Edition from the Technical Reviewers "A refreshing industrial flavor. Design concepts are presented as they are needed for 'just-in-time' learning. Simulating and designing circuits using SPICE is emphasized with literally hundreds of examples. Very few textbooks contain as much detail as this one. Highly recommended!" --Paul M. Furth, New Mexico State University "This book builds a solid knowledge of CMOS circuit design from the ground up. With coverage of process integration, layout, analog and digital models, noise mechanisms, memory circuits, references, amplifiers, PLLs/DLLs, dynamic circuits, and data converters, the text is an excellent reference for both experienced and novice designers alike." --Tyler J. Gomm, Design Engineer, Micron Technology, Inc. "The Second Edition builds upon the success of the first with new chapters that cover additional material such as oversampled converters and non-volatile memories. This is becoming the de facto standard textbook to have on every analog and mixed-signal designer's bookshelf." --Joe Walsh, Design Engineer, AMI Semiconductor CMOS circuits from design to implementation CMOS: Circuit Design, Layout, and Simulation, Revised Second Edition covers the practical design of both analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and much more. This edition takes a two-path approach to the topics: design techniques are developed for both long- and short-channel CMOS technologies and then compared. The results are multidimensional explanations that allow readers to gain deep insight into the design process. Features include: Updated materials to reflect CMOS technology's movement into nanometer sizes Discussions on phase- and delay-locked loops, mixed-signal circuits, data converters, and circuit noise More than 1,000 figures, 200 examples, and over 500 end-of-chapter problems In-depth coverage of both analog and digital circuit-level design techniques Real-world process parameters and design rules The book's Web site, CMOSedu.com, provides: solutions to the book's problems; additional homework problems without solutions; SPICE simulation examples using HSPICE, LTspice, and WinSpice; layout tools and examples for actually fabricating a chip; and videos to aid learning

SKREW-TOLERANT CIRCUIT DESIGN

[Morgan Kaufmann](#) As advances in technology and circuit design boost operating frequencies of microprocessors, DSPs and other fast chips, new design challenges continue to emerge. One of the major performance limitations in today's chip designs is clock skew, the uncertainty in arrival times between a pair of clocks. Increasing clock frequencies are forcing many engineers to rethink their timing budgets and to use skew-tolerant circuit techniques for both domino and static circuits. While senior designers have long developed their own techniques for reducing the sequencing overhead of domino circuits, this knowledge has routinely been protected as trade secret and has rarely been shared. Skew-Tolerant Circuit Design presents a systematic way of achieving the same goal and puts it in the hands of all designers. This book clearly presents skew-tolerant techniques and shows how they address the challenges of clocking, latching, and clock skew. It provides the practicing circuit designer with a clearly detailed tutorial and an insightful summary of the most recent literature on these critical clock skew issues. * Synthesizes the most recent advances in skew-tolerant design in one cohesive tutorial * Provides incisive instruction and advice punctuated by humorous illustrations * Includes exercises to test understanding of key concepts and solutions to selected exercises

EARTHQUAKE RESISTANT DESIGN AND RISK REDUCTION

[John Wiley & Sons](#) **Earthquake Resistant Design and Risk Reduction**, 2nd edition is based upon global research and development work over the last 50 years or more, and follows the author's series of three books Earthquake Resistant Design, 1st and 2nd editions (1977 and 1987), and Earthquake Risk Reduction (2003). Many advances have been made since the 2003 edition of Earthquake Risk Reduction, and there is every sign that this rate of progress will continue apace in the years to come. Compiled from the author's wide design and research experience in earthquake engineering and engineering seismology, this key text provides an excellent treatment of the complex multidisciplinary process of earthquake resistant design and risk reduction. New topics include the creation of low-damage structures and the spatial distribution of ground shaking near large fault ruptures. Sections on guidance for developing countries, response of buildings to differential settlement in liquefaction, performance-based and displacement-based design and the architectural aspects of earthquake resistant design are heavily revised. This book: Outlines individual national weaknesses that contribute to earthquake risk to people and property Calculates the seismic response of soils and structures, using the structural continuum "Subsoil - Substructure - Superstructure - Non-structure" Evaluates the effectiveness of given design and construction procedures for reducing casualties and financial losses Provides guidance on the key issue of choice of structural form Presents earthquake resistant design methods for the main four structural materials - steel, concrete, reinforced masonry and timber - as well as for services equipment, plant and non-structural architectural components Contains a chapter devoted to problems involved in improving (retrofitting) the existing built environment This book is an invaluable reference and guiding tool to practising civil and structural engineers and architects, researchers and postgraduate students in earthquake engineering and engineering seismology, local governments and risk management officials.

AN INTRODUCTION TO VLSI PHYSICAL DESIGN

[McGraw-Hill College](#)

DIGITAL INTEGRATED CIRCUITS

A DESIGN PERSPECTIVE

Beginning with discussions on the operation of electronic devices and analysis of the nucleus of digital design, the text addresses: the impact of interconnect, design for low power, issues in timing and clocking, design methodologies, and the effect of design automation on the digital design perspective.

BEST PRACTICES FOR GRAPHIC DESIGNERS, GRIDS AND PAGE LAYOUTS

AN ESSENTIAL GUIDE FOR UNDERSTANDING AND APPLYING PAGE DESIGN PRINCIPLES

[Rockport Pub](#) This unique, go-to guide for designers fully details the essential layout and design skills needed to succeed in this competitive industry. With fun and practical application, it offers valuable insight into strategy and business when working in the real world with real clients, starting with basic information on layout principles before delving more deeply into theory and application on a project-by-project basis. Illustrated with real-world assignments and case studies, this guide offers a behind-the-scenes take on the entire process and steps necessary to go from concept to final outcome, including how to overcome challenges presented along the way.

VLSI TEST PRINCIPLES AND ARCHITECTURES

DESIGN FOR TESTABILITY

[Elsevier](#) This book is a comprehensive guide to new DFT methods that will show the readers how to design a testable and quality product, drive down test cost, improve product quality and yield, and speed up time-to-market and time-to-volume. Most up-to-date coverage of design for testability. Coverage of industry practices commonly found in commercial DFT tools but not discussed in other books. Numerous, practical examples in each chapter illustrating basic VLSI test principles and DFT architectures.

VLSI FABRICATION PRINCIPLES

SILICON AND GALLIUM ARSENIDE

Wiley-Interscience In some places, the order of presentation has been changed to fine-tune the book's effectiveness as a senior and graduate-level teaching text. Fabrication principles covered include those for such circuits as CMOS, BIPOLAR, BICMOS, FET, and more.

PHYSICAL DESIGN ESSENTIALS

AN ASIC DESIGN IMPLEMENTATION PERSPECTIVE

Springer Science & Business Media Arranged in a format that follows the industry-common ASIC physical design flow, Physical Design Essentials begins with general concepts of an ASIC library, then examines floorplanning, placement, routing, verification, and finally, testing. Among the topics covered are Basic standard cell design, transistor-sizing, and layout styles; Linear, non-linear, and polynomial characterization; Physical design constraints and floorplanning styles; Algorithms used for placement; Clock Tree Synthesis; Parasitic extraction; Electronic Testing, and many more.

VLSI PLANARIZATION

METHODS, MODELS, IMPLEMENTATION

Springer Science & Business Media At the beginning we would like to introduce a refinement. The term 'VLSI planarization' means planarization of a circuit of VLSI, i.e. the embedding of a VLSI circuit in the plane by different criteria such as the minimum number of connectors, the minimum total length of connectors, the minimum number of over-the-element routes, etc. A connector is designed to connect the broken sections of a net. It can be implemented in different ways depending on the technology. Connectors for a bipolar VLSI are implemented by diffused tunnels, for instance. By over-the-element route we shall mean a connection which intersects the enclosing rectangle of an element (or a cell). The possibility of the construction such connections during circuit planarization is reflected in element models and can be ensured, for example, by the availability of areas within the rectangles where connections may be routed. VLSI planarization is one of the basic stages (others will be discussed below) of the so called topological (in the mathematical sense) approach to VLSI design. This approach does not lie in the direction of the classical approach to automation of VLSI layout design. In the classical approach to computer aided design the placement and routing problems are solved successively. The topological approach, in contrast, allows one to solve both problems at the same time. This is achieved by constructing a planar embedding of a circuit and obtaining the proper VLSI layout on the basis of it.

LOW-POWER CMOS VLSI CIRCUIT DESIGN

John Wiley & Sons This is the first book devoted to low power circuit design, and its authors have been among the first to publish papers in this area. Low-Power CMOS VLSI Design· Physics of Power Dissipation in CMOS FET Devices· Power Estimation· Synthesis for Low Power· Design and Test of Low-Voltage CMOS Circuits· Low-Power Static Ram Architectures· Low-Energy Computing Using Energy Recovery Techniques· Software Design for Low Power

DIGITAL LOGIC AND COMPUTER DESIGN

Pearson Education India This book presents the basic concepts used in the design and analysis of digital systems and introduces the principles of digital computer organization and design.

CMOS DIGITAL INTEGRATED CIRCUITS

ANALYSIS AND DESIGN

The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability.

PROCEEDINGS OF 2ND INTERNATIONAL CONFERENCE ON MICRO-ELECTRONICS, ELECTROMAGNETICS AND TELECOMMUNICATIONS

ICMEET 2016

Springer The book is a collection of best papers presented in the Second International Conference on Microelectronics Electromagnetics and Telecommunication (ICMEET 2016), an international colloquium, which aims to bring together academic scientists, researchers and research scholars to discuss the recent developments and future trends in the fields of microelectronics, electromagnetics and telecommunication. Microelectronics research investigates semiconductor materials and device physics for developing electronic devices and integrated circuits with data/energy efficient performance in terms of speed, power consumption, and functionality. The book discusses various topics like analog, digital and mixed signal circuits, bio-medical circuits and systems, RF circuit design, microwave and millimeter wave circuits, green circuits and systems, analog and digital signal processing, nano electronics and giga scale systems, VLSI circuits and systems, SoC and NoC, MEMS and NEMS, VLSI digital signal processing, wireless communications, cognitive radio, and data communication.

SOLID STATE DEVICES

PHI Learning Pvt. Ltd. Designed as a text for undergraduate students of engineering in Electrical, Electronics, and Computer Science and IT disciplines as well as undergraduate students (B.Sc.) of physics and electronics as also for postgraduate students of physics and electronics, this compact and accessible text endeavours to simplify the theory of solid state devices so that even an average student will be able to understand the concepts with ease. The authors, Prof. Somanathan Nair and Prof. S.R. Deepa, with their rich and long experience in teaching the subject, provide a detailed discussion of such topics as crystal structures of semiconductor materials, Miller indices, energy band theory of solids, energy level diagrams and mass action law. Besides, they give a masterly analysis of topics such as direct and indirect gap materials, Fermi-Dirac statistics, electrons in semiconductors, Hall effect, PN junction diodes, Zener and avalanche breakdowns, Schottky barrier diodes, bipolar junction transistors, MOS field-effect transistors, Early effect, Shockley diodes, SCRs, TRIAC, and IGBTs. In the Second Edition, two new chapters on opto-electronic devices and electro-optic devices have been added. The text has been thoroughly revised and updated. A number of solved problems and objective type questions have been included to help students develop grasp of the contents. This fully illustrated and well-organized text should prove invaluable to students pursuing various courses in engineering and physics. **DISTINGUISHING FEATURES** • Discusses the concepts in an easy-to-understand style. • Furnishes over 300 clear-cut diagrams to illustrate the discussed. • Gives a very large number of questions—short answer, fill in the blanks, tick the correct answer and review questions—to sharpen the minds of the reader. • Provides more than 200 fully solved numerical problems. • Gives answers to a large number of exercises.

DESIGN THROUGH VERILOG HDL

[John Wiley & Sons](#) A comprehensive resource on Verilog HDL for beginners and experts Large and complicated digital circuits can be incorporated into hardware by using Verilog, a hardware description language (HDL). A designer aspiring to master this versatile language must first become familiar with its constructs, practice their use in real applications, and apply them in combinations in order to be successful. Design Through Verilog HDL affords novices the opportunity to perform all of these tasks, while also offering seasoned professionals a comprehensive resource on this dynamic tool. Describing a design using Verilog is only half the story: writing test-benches, testing a design for all its desired functions, and how identifying and removing the faults remain significant challenges. Design Through Verilog HDL addresses each of these issues concisely and effectively. The authors discuss constructs through illustrative examples that are tested with popular simulation packages, ensuring the subject matter remains practically relevant. Other important topics covered include: Primitives Gate and Net delays Buffers CMOS switches State machine design Further, the authors focus on illuminating the differences between gate level, data flow, and behavioral styles of Verilog, a critical distinction for designers. The book's final chapters deal with advanced topics such as timescales, parameters and related constructs, queues, and switch level design. Each chapter concludes with exercises that both ensure readers have mastered the present material and stimulate readers to explore avenues of their own choosing. Written and assembled in a paced, logical manner, Design Through Verilog HDL provides professionals, graduate students, and advanced undergraduates with a one-of-a-kind resource.

MODERN VLSI DESIGN

SYSTEM-ON-CHIP DESIGN

[Pearson Education](#) For Electrical Engineering and Computer Engineering courses that cover the design and technology of very large scale integrated (VLSI) circuits and systems. May also be used as a VLSI reference for professional VLSI design engineers, VLSI design managers, and VLSI CAD engineers. Modern VLSI Design provides a comprehensive "bottom-up" guide to the design of VLSI systems, from the physical design of circuits through system architecture with focus on the latest solution for system-on-chip (SOC) design. Because VLSI system designers face a variety of challenges that include high performance, interconnect delays, low power, low cost, and fast design turnaround time, successful designers must understand the entire design process. The Third Edition also provides a much more thorough discussion of hardware description languages, with introduction to both Verilog and VHDL. For that reason, this book presents the entire VLSI design process in a single volume.

A TEXTBOOK OF APPLIED ELECTRONICS

[S. Chand Publishing](#) The present book has been thoroughly revised and lot of useful material has been added .several photographs of electronic devices and their specifications sheets have been included.This will help the students to have a better understanding of the electrinic devices and circuits from application point of view.the mistake and misprints,which has crept in,have been eliminated in this edition.
